



**INRIA INNOVATION LAB  
CERTIVIBE  
V1.0**

**SOFTWARE REQUIREMENT SPECIFICATION**

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## 1. Purpose

The purpose of this document is to list CertiViBE software requirements.

## 2. Scope

Requirements will be divided into two families:

- Functional requirements describing features or services delivered by the software;
- Non-functional requirements describing operational constraints and behavior (e.g. security, performance, availability, maintainability, certifiability...).

## 3. References

DOCUMENT #	TITLE
EN ISO 13485	Quality systems - Medical devices - System requirements for regulatory purposes
EN ISO 14971	Medical devices - Application of risk management to medical devices
IEC 62304	Medical device software - Software lifecycle processes
MEDDEV 2.1/6 January 2012	Qualification and Classification of standalone software

## 4. Definitions

Specifications	A specification is defined as “a document that states requirements.” There are many different kinds of written specifications, e.g., system requirements specification, software requirements specification, software design specification, software test specification, software integration specification, etc.
Requirements	A requirement is a need, expectation, or obligation. It can be stated or implied by an organization, its customers, or other interested parties.  A specified requirement is one that has been stated (in a document for example), whereas an implied requirement is a need, expectation, or obligation that is common practice or customary.. There can be many different kinds of requirements (e.g., design, functional, implementation, interface, performance, or physical requirements).

Verification	Verification is a process. It uses objective evidence to confirm that specified requirements have been met. Whenever specified requirements have been met, a verified status is achieved. Objective evidence can be collected by performing observations, measurements, tests, or using other suitable methods.
Validation	Validation is a process. It uses objective evidence to confirm that the requirements which define an intended use or application have been met. Whenever all requirements have been met, a validated status is established. Validation can be carried out under realistic use conditions or within a simulated use environment.
Intended purpose	'Intended purpose' means the use for which the device is intended according to the data supplied by the manufacturer on the labelling, in the instructions and/or in promotional materials.

## 5. Abbreviations

DHF	Design History File	Dossier containing all documents and items relating to software design and development process: this is the chronological history of the development.
DD	Design Dossier	Dossier containing the documents and items related to the software itself
DCR	Design Change Request	
IL	Innovation Lab	Inria innovation lab CertiViBE
CC	Critical Characteristic	
SD	Software Definition	
SRS	Software Requirements Specification	
RSK	Risk evaluation	
RQ	Regulatory Quality	

## 6. Technical glossary

<b>BCI</b>	Brain computer Interface
<b>GUI</b>	Graphical User Interface
<b>API</b>	Application Programming Interface
<b>EEG</b>	Electroencephalography

## 7. General Behavior

The CertiViBE software is based upon the OpenViBE software, and could be seen as the core feature OpenViBE.

The goal of CertiViBE software is to create or load an existing processing pipeline for BCI purpose, and especially for the Neurofeedback applications. The software can load, process and save EEG data.

A processing pipeline could be seen as a scenario with several plugin (boxes) connected, to process EEG data provided to the scenario. The scenario is the way to manage the EEG signal, and for example extract relevant signal properties you want to use or monitor.

The plugin also call boxes are a collection of features that can perform mathematical treatment, classification, signal processing, signal acquisition etc. One of the main idea is that final user could develop new plugin to add features instead of rewriting all CertiViBE.

The CertiViBE software won't provide a GUI, a tool to create scenario or an acquisition device, but it will provide tools, API and documentation for building EEG signal processing systems.

For the validation and verification purpose, CertiViBE will also have testing tool to launch existing scenario.

## 8. Functional requirement

This section describes functional requirements involved in CertiViBE software.

### 8.1 General Requirements

<b>Tag # / CC</b>	SRS-001
<b>Requirement</b>	<p>The system must provide functionalities to manage a data processing pipelines. A data processing pipeline is a collection of interconnected processing algorithms/filters. The system must be able to:</p> <ul style="list-style-type: none"><li>- Declare and create a pipelines</li><li>- Add/Remove filters to/from a pipeline</li><li>- Connect/Disconnect filters</li><li>- Delete a pipeline</li></ul>
<b>Acceptance Criteria</b>	Create a processing pipeline, add 3 compatible filters, connect the 3 filters, remove the last one and delete the pipeline.
<b>Rationale</b>	Flexibility to be able to build a personalized processing pipeline from individual filters.
<b>Reference (SD,SRS,RSK)</b>	SD-04
<b>Priority</b>	-
<b>Criticality</b>	-
<b>Non-functional RQ</b>	-

<b>Tag # / CC</b>	SRS-002
<b>Requirement</b>	<p>The system must provide functionalities to configure a processing pipeline. Each filter in the processing pipeline must provide a configurability state. If the filter is configurable, then it must be possible to update its configuration.</p> <p><u>Assumption / Prerequisites:</u></p> <p>Requirement SRS-001 is full filled.</p>
<b>Acceptance Criteria</b>	Create a processing pipeline, add one configurable filters, and modify its settings.
<b>Rationale</b>	Flexibility through late-binding.
<b>Reference (SD,SRS,RSK)</b>	SRS-001, SD-04
<b>Priority</b>	-
<b>Criticality</b>	-
<b>Non-functional RQ</b>	-



<b>Tag # / CC</b>	SRS-003
<b>Requirement</b>	<p>The system must provide functionalities to execute one or more processing pipelines in parallel. It includes:</p> <ul style="list-style-type: none"> <li>- Starting/Stopping the execution</li> <li>- Pausing the pipeline execution</li> <li>- Executing the pipeline as quickly as possible</li> <li>- Executing the pipeline step by step pausing after each step</li> </ul> <p>Execution pipeline can be controlled directly or on stimulation receiving.</p> <p><u>Assumption / Prerequisites :</u></p> <p>Requirement SRS-001 is fulfilled.</p>
<b>Acceptance Criteria</b>	Create at least two processing pipelines, start a pipeline execution, pause the pipeline execution, execute the pipeline step-by-step, restart the execution as quickly as possible, stop the execution. Do the same for other pipeline(s). All processing pipelines should be able to be executed independently in parallel.
<b>Rationale</b>	Flexibility through different level of execution.
<b>Reference (SD,SRS,RSK)</b>	SRS-002, SD-04, SD-41
<b>Priority</b>	-
<b>Criticality</b>	-
<b>Non-functional RQ</b>	1. Configurability (For control on stimulation receiving): Input stimulation type to trigger control, type of control.

<b>Tag # / CC</b>	SRS-004
<b>Requirement</b>	The system must provide functionalities for late-binding modifications. A user should be able to modify or create configuration tokens without recompiling the system. Late-binding modification must be provided through an editable resource file.
<b>Acceptance Criteria</b>	User can create a pipeline and write a configuration file for it which will be read when the pipeline is executed.
<b>Rationale</b>	Late-binding configurability brings flexibility by relieving the user from recompiling the system each time a configuration setting is modified.
<b>Reference (SD,SRS,RSK)</b>	SD-07
<b>Priority</b>	-
<b>Criticality</b>	-
<b>Non-functional RQ</b>	-

<b>Tag # / CC</b>	SRS-005
<b>Requirement</b>	<p>The system must provide functionalities to log traces of APIs calls.</p> <p>Log management must provide at least 2 level of activations:</p> <ul style="list-style-type: none"> <li>- One for debugging activities</li> <li>- One for production activities</li> </ul> <p>Log must be provided on 2 different types of output:</p> <ul style="list-style-type: none"> <li>- Console output</li> <li>- File output</li> </ul> <p>Creating a new type of output must be possible and must be an add-only process that does not require to modify any existing features.</p> <p>The source of each log entry must be clearly identifiable and activation level should be modifiable at runtime.</p>
<b>Acceptance Criteria</b>	<p>Create a processing pipeline, execute the pipeline with different activation level and check some logs are written into the file accordingly.</p> <p>Check the design allows the addition of a new type of output without any modification of the current implementation.</p>
<b>Rationale</b>	Traces are necessary for debugging and software usage analysis.
<b>Reference (SD,SRS,RSK)</b>	SD-07
<b>Priority</b>	-
<b>Criticality</b>	-
<b>Non-functional RQ</b>	-

## 8.2 Acquisition Requirements

<b>Tag # / CC</b>	SRS-006
<b>Requirement</b>	The system shall provide a way to connect to an acquisition device. This method should be extensible. Kernel must be able to synchronize its clock with the acquisition system.
<b>Acceptance Criteria</b>	The user will create a pipeline which acquires data and prints it. Kernel is instructed to connect to an acquisition device and the pipeline is executed. The data from the device must be produced as it arrived by the pipeline.
<b>Rationale</b>	Connection to an acquisition device is necessary to retrieve input data for further processing.
<b>Reference (SD,SRS,RSK)</b>	SD-52
<b>Priority</b>	-
<b>Criticality</b>	-
<b>Non-functional RQ</b>	1. Extensibility, Interoperability: The system should consider all incoming samples. If the system is connected to several devices, then their clocks must be synchronized.

<b>Tag # / CC</b>	SRS-007
<b>Requirement</b>	The acquisition system should be able to connect to the OpenViBE Acquisition Server.  <u>Assumption / Prerequisites:</u> Requirement SRS-006 is fulfilled.
<b>Acceptance Criteria</b>	Test in SRS-006 shall be conducted using the OpenViBE Acquisition Server and a simulated device driver (Oscillator).
<b>Rationale</b>	The acquisition server is considered as another driver from the acquisition device perspective.
<b>Reference (SD,SRS,RSK)</b>	SD-09, SRS-006, SD-52
<b>Priority</b>	-
<b>Criticality</b>	-
<b>Non-functional RQ</b>	1. Robustness: The flow coming from the Acquisition Server must be considered “perfect” in timings.

### 8.3 File I/O Requirements

<b>Tag # / CC</b>	SRS-008
<b>Requirement</b>	The system must provide functionalities to store (and then to retrieve) data output by filters at any time in the processing pipeline. Data will be stored in a binary file and can contain data from multiple outputs at the same time.
<b>Acceptance Criteria</b>	A known signal is read by the “File Reader” component from a file. This file is then written by the “File Writer” component. The written signal must be equal to the original.
<b>Rationale</b>	Storage of data is important for archiving and offline analysis.
<b>Reference (SD,SRS,RSK)</b>	SRS-521, SRS-523, SD-16, SD-17
<b>Priority</b>	-
<b>Criticality</b>	-
<b>Non-functional RQ</b>	-

<b>Tag # / CC</b>	SRS-038
<b>Requirement</b>	The system must provide functionalities to store (and then to retrieve) matrices and stimulations at any time in the processing pipeline. Data will be stored in a csv format.
<b>Acceptance Criteria</b>	A known signal is read by the “CSV File Reader” component from a file. This file is then written by the “CSV File Writer” component. The written signal must be equal to the original.
<b>Rationale</b>	Storage of data outputs by filters in a textual format is important for debugging and testing purposes.
<b>Reference (SD,SRS,RSK)</b>	SRS-521, SRS-523, SD-53, SD-54
<b>Priority</b>	-
<b>Criticality</b>	-
<b>Non-functional RQ</b>	-

<b>Tag # / CC</b>	SRS-009
<b>Requirement</b>	The system must provide functionalities to store (and then to retrieve) the whole processing pipeline (structure and configuration). The pipeline is stored in xml format following a pre-defined structure (DTD or XML Schema).
<b>Acceptance Criteria</b>	Create a processing pipeline and save it in XML format. Then load it and save it again with another name. Check both saved pipelines are identical.
<b>Rationale</b>	Saving processing pipelines is needed for reusability of task-oriented pipelines and testability.
<b>Reference (SD,SRS,RSK)</b>	SD-04, SRS-520, SRS-522
<b>Priority</b>	-
<b>Criticality</b>	-
<b>Non-functional RQ</b>	-

<b>Tag # / CC</b>	SRS-010
<b>Requirement</b>	The system must provide functionalities to store (and then to retrieve) filter and any signal processing module settings. The settings are stored in xml format following a pre-defined structure (DTD or XML Schema).
<b>Acceptance Criteria</b>	A processing pipeline is created containing a filter with default settings. A file containing new settings for this filter is created alongside. Upon execution of the pipeline, the filter should use the settings from the file.
<b>Rationale</b>	Saving filter settings is needed for reusability of task-oriented pipelines and testability.
<b>Reference (SD,SRS,RSK)</b>	SD-04
<b>Priority</b>	-
<b>Criticality</b>	-
<b>Non-functional RQ</b>	-

## 8.4 Data Generation Requirements

<b>Tag # / CC</b>	SRS-011
<b>Requirement</b>	The system must provide functionalities to generate a linear signal ( $y$ (amplitude) = $x$ (time)) on a single channel.
<b>Acceptance Criteria</b>	The source of signal is placed in a scenario with a spy connected to it. When the scenario is executed, the spy should see the incoming signal and validate that its level is correct.
<b>Rationale</b>	This type of data is used for testing purpose when no data is available.
<b>Reference (SD,SRS,RSK)</b>	SD-04, SD-19
<b>Priority</b>	-
<b>Criticality</b>	-
<b>Non-functional RQ</b>	1. Configurability: Sampling frequency.

<b>Tag # / CC</b>	SRS-012
<b>Requirement</b>	The system must provide functionalities to generate periodic stimulations.
<b>Acceptance Criteria</b>	The source of stimulations is placed in a scenario with a spy connected to it. When the scenario is executed, the spy should see the incoming stimulations.
<b>Rationale</b>	Some experiments/treatments require periodic impulses to drive stimulation, e.g.: auditory beeps.
<b>Reference (SD,SRS,RSK)</b>	SD-04, SD-18
<b>Priority</b>	-
<b>Criticality</b>	-
<b>Non-functional RQ</b>	1. Configurability: Output stimulation type and sampling frequency.





<b>Tag # / CC</b>	SRS-013
<b>Requirement</b>	The system must provide functionalities to generate a stimulation after a period of time without receiving any signal.
<b>Acceptance Criteria</b>	A file reader is placed in a scenario along with the algorithm detecting periods without signal. When the scenario is executed, a stimulation should be triggered after all of the signal has been read plus the “timeout” amount.
<b>Rationale</b>	Sometimes it is important to know that no more data is incoming. This can indicate, for example, an end of a file. Since the streams inputs are not necessarily continuous, it is important to be able to set the time after which the signal stream is considered finished.
<b>Reference (SD,SRS,RSK)</b>	SD-04, SD-44
<b>Priority</b>	-
<b>Criticality</b>	-
<b>Non-functional RQ</b>	1. Configurability: Timeout delay and output stimulation type

<b>Tag # / CC</b>	SRS-014
<b>Requirement</b>	The system must provide functionalities to generate a stimulation when the input signal sign changes.
<b>Acceptance Criteria</b>	Scenario is set to read artificial signal that crosses the 0 level at a known time. The system should trigger a stimulation at this moment.
<b>Rationale</b>	Many systems are based on detecting that the signal has reached a certain threshold. This function combined with linear algebra can detect any signal level.
<b>Reference (SD,SRS,RSK)</b>	SD-04, SD-45
<b>Priority</b>	-
<b>Criticality</b>	-
<b>Non-functional RQ</b>	1. Configurability: Output stimulation type on switch to positive value, output stimulation type on switch to negative value, index of the channel to operate on.



## 8.5 Data Aggregation Requirements

<b>Tag # / CC</b>	SRS-015
<b>Requirement</b>	The system must provide functionalities to merge several signals into one, provided they are sampled in the same manner.
<b>Acceptance Criteria</b>	Artificial signal is read by the scenario; the signal is separated into channels. A mathematical formula (the identity) is applied to each channel's signal. After that the signal is merged. Each channel of the resulting signal must be equivalent to the individual signal of each channels before the split.
<b>Rationale</b>	Signal can have several sources, sometimes this is due to the fact that part of it is processed in a different way from the rest - the incoming signal is split. We need to be able to merge the signal back to obtain a single flow.
<b>Reference (SD,SRS,RSK)</b>	SD-04, SD-46, SD-47, SRS-022
<b>Priority</b>	-
<b>Criticality</b>	-
<b>Non-functional RQ</b>	1. Configurability: This algorithm is only required to merge signals with the same metrics.

<b>Tag # / CC</b>	SRS-016
<b>Requirement</b>	The system must provide functionalities to merge several stimulation streams into one stream. The resulting stream must contain all input stimulation ordered by start date.
<b>Acceptance Criteria</b>	Two known streams of stimulations are merged. The resulting stream of stimulations must contain the correct stimulations.
<b>Rationale</b>	Stimulations can come from several sources. However sometimes they have to be treated in a single stream.
<b>Reference (SD,SRS,RSK)</b>	SD-04, SD-42
<b>Priority</b>	-
<b>Criticality</b>	-
<b>Non-functional RQ</b>	1. Robustness: The resulting stream of stimulations must be ordered correctly and no stimulations can be lost.

## 8.6 Classification Requirements

<b>Tag # / CC</b>	SRS-017
<b>Requirement</b>	<p>The system must provide functionalities to classify data given features. The system shall be extensible and provide a way to choose the classification algorithm at runtime.</p> <p>Both phases of classification must be available:</p> <ol style="list-style-type: none"> <li>1. Training phase to build training classes</li> <li>2. Testing/Prediction phase used to classify signal features</li> </ol> <p>The following multiclass strategies must be available:</p> <ol style="list-style-type: none"> <li>1. One-vs.-all</li> <li>2. One-vs.-one</li> <li>3. Native multiclass</li> </ol> <p>Adding a new classifier must be possible and must be an add-only process that does not require to modify any existing features.</p>
<b>Acceptance Criteria</b>	<p>A known signal presenting two distinguishable features during known periods of time is generated and used in a classifier training pipeline. This pipeline must generate a configuration for a classifier implementation.</p> <p>The same signal is then used in a second pipeline and its features are extracted to feed the classifier configured in the previous step. The classifier must label the signal with correct classes.</p> <p>Check the design allows the addition of a new classifiers without any modification of the current implementation.</p>
<b>Rationale</b>	<p>Successful classification is one of the primary uses of the CertiViBE toolkit. A classification problem has frequently more than two classes. It is thus important to have a solution for multi-class classifiers.</p>
<b>Reference (SD,SRS,RSK)</b>	SD-04, SD-13, SD-14, SD-15, SD-51
<b>Priority</b>	-
<b>Criticality</b>	-
<b>Non-functional RQ</b>	<ol style="list-style-type: none"> <li>1. Performance: The overhead of the classifier structure should be negligible in comparison with the classifier algorithm processing.</li> </ol>

<b>Tag # / CC</b>	SRS-018
<b>Requirement</b>	The system must provide functionalities to classify data using the Regularized LDA (Linear Discriminant Analysis) algorithm.
<b>Acceptance Criteria</b>	The tests of SRS-17 will be performed using an LDA classifier implementation.
<b>Rationale</b>	LDA classifier is a very good starting point for any classification problem.
<b>Reference (SD,SRS,RSK)</b>	SD-04
<b>Priority</b>	-
<b>Criticality</b>	-
<b>Non-functional RQ</b>	1. Performance: The implemented LDA classifier must have state of the art performance. Ref: <a href="http://statweb.stanford.edu/~tibs/ElemStatLearn/">http://statweb.stanford.edu/~tibs/ElemStatLearn/</a>

## 8.7 Signal Processing Requirements

<b>Tag # / CC</b>	SRS-019
<b>Requirement</b>	The system must provide functionalities to average data represented as time series (signal, spectra, and feature vectors for classification). The averaging shall be possible on channel wise and sample wise.
<b>Acceptance Criteria</b>	A known signal of various types is produced (signal, spectra) and averaged. The resulting data must be correct.
<b>Rationale</b>	There are many applications for averaging. One of them is to increase robustness of a system by using multiple data points over time.
<b>Reference (SD,SRS,RSK)</b>	SD-04, SD-22, SD-31, SD-39
<b>Priority</b>	-
<b>Criticality</b>	-
<b>Non-functional RQ</b>	-

<b>Tag # / CC</b>	SRS-020
<b>Requirement</b>	The system must provide functionalities to reference the signal according to several methods: <ul style="list-style-type: none"> <li>• Common average reference</li> <li>• Single channel reference</li> </ul>
<b>Acceptance Criteria</b>	References shall be tested by applying them to known signal and comparing the results to pre-calculated values.
<b>Rationale</b>	Non-referenced signal contains an important amount of noise. Referencing the signal to a source helps removing it.
<b>Reference (SD,SRS,RSK)</b>	SD-04, SD-34, SD-36
<b>Priority</b>	-
<b>Criticality</b>	-
<b>Non-functional RQ</b>	1. Performance: Performance should be linearly dependent on the number of channels and sampling rate.



<b>Tag # / CC</b>	SRS-021
<b>Requirement</b>	The system must provide functionalities to rename channels of a multichannel time series.
<b>Acceptance Criteria</b>	A stream of channels with known names is read by the renaming algorithm and names are renamed to a different known values. The resulting signal must be identical with exception of the names.
<b>Rationale</b>	Avoiding channel name collision with channels coming from different signals.
<b>Reference (SD,SRS,RSK)</b>	SD-04, SD-33
<b>Priority</b>	-
<b>Criticality</b>	-
<b>Non-functional RQ</b>	<ol style="list-style-type: none"> <li>1. Performance: Renaming of channels should not have any impact on performance of the system.</li> <li>2. Configurability: the list containing the new names of the channels</li> </ol>

<b>Tag # / CC</b>	SRS-022
<b>Requirement</b>	The system must provide functionalities to select a subset of channels from a multichannel time series. Removal can be performed on a name or index basis.
<b>Acceptance Criteria</b>	A signal with known channels is used as an input to a pipeline. Remove the first channel by index and the last one by name.  The pipelines output is signal that only contains all channels but the first and last one.
<b>Rationale</b>	The incoming signal may contain undesirable channels, e.g.: non-connected channels, noisy channels, non-EEG channels etc.
<b>Reference (SD,SRS,RSK)</b>	SD-04, SD-32
<b>Priority</b>	-
<b>Criticality</b>	-
<b>Non-functional RQ</b>	1. Configurability: The channels to be retained or removed from the stream

<b>Tag # / CC</b>	SRS-023
<b>Requirement</b>	The system must provide functionalities to threshold the signal.
<b>Acceptance Criteria</b>	A signal with known amplitude is thresholded with a maximum/minimum threshold. The amplitude of the resulting signal must not be higher/lower than the maximum/minimum demanded value.
<b>Rationale</b>	Signal can present artifacts that can be removed using cropping.
<b>Reference (SD,SRS,RSK)</b>	SD-04, SD-30, SD-024
<b>Priority</b>	-
<b>Criticality</b>	-
<b>Non-functional RQ</b>	1. Configurability: Thresholding method (minimum thresholding, maximum thresholding, both thresholding), thresholding values.

<b>Tag # / CC</b>	SRS-024
<b>Requirement</b>	<p>The system must provide functionalities to apply mathematical formulae to each sample of a signal or combination of signals. The system can either perform a mathematical operation on two signals, or a conditional selection between two signals.</p> <p>The required functionalities are:</p> <ul style="list-style-type: none"> <li>- Mathematical: +, -, *, /, abs, acos, asin, atan, ceil, cos, exp, floor, log, log10, sin, sqrt, tan, pow</li> <li>- Comparison: &gt;, &gt;=, &lt;, =, !=</li> <li>- Boolean: and, or, not, xor</li> </ul>
<b>Acceptance Criteria</b>	All of the available functions must produce correct values. A known signal will be sent to the mathematical processor and the results will be recorded for each of the available operators. The results will be compared to a previously calculated and validated values.
<b>Rationale</b>	
<b>Reference (SD,SRS,RSK)</b>	SD-04, SD-23
<b>Priority</b>	-
<b>Criticality</b>	-
<b>Non-functional RQ</b>	<ol style="list-style-type: none"> <li>1. Configurability: Formula to apply.</li> <li>2. Performance: The computing time should scale linearly.</li> </ol>

<b>Tag # / CC</b>	SRS-025
<b>Requirement</b>	The system must provide functionalities to re sample (down sample or up sample) the signal.
<b>Acceptance Criteria</b>	Known source signal is re sampled by several different factors and compared to the same signal re sampled by a different, known and tested method.
<b>Rationale</b>	Some tasks are very time consuming if they are done on a signal with high or low sampling rate. For this purpose it is necessary to be able to re sample the signal.
<b>Reference (SD,SRS,RSK)</b>	SD-04, SD-40, SD-21
<b>Priority</b>	-
<b>Criticality</b>	-
<b>Non-functional RQ</b>	<ol style="list-style-type: none"> <li>1. Configurability: New sampling rate.</li> <li>2. Robustness: Re sampling should not introduce artifacts to the signal, beyond what is accepted in the state of the art (in terms of delay and phase shifting).</li> </ol>

<b>Tag # / CC</b>	SRS-026
<b>Requirement</b>	The system must provide functionalities to temporally filter the signal. The system should provide low-pass, high-pass, band-pass and band-stop filters. The method of filtering should be configurable.
<b>Acceptance Criteria</b>	A known signal will be temporally filtered using the four filter methods and the results compared to the same signal filtered with a known and tested method. The results must be identical with leeway for rounding errors.
<b>Rationale</b>	Temporal filtering is the basis for a great number of signal processing processes, feature extraction and de-noising to name a few.
<b>Reference (SD,SRS,RSK)</b>	SD-04
<b>Priority</b>	-
<b>Criticality</b>	-
<b>Non-functional RQ</b>	<ol style="list-style-type: none"> <li>1. Configurability: Boundary frequencies, filtering type.</li> <li>2. Robustness: Temporal filtering should not introduce artifacts to the signal, beyond what is accepted in the state of the art (in terms of delay and phase shifting).</li> </ol>

<b>Tag # / CC</b>	SRS-027
<b>Requirement</b>	The system must provide the Butterworth method for temporal filtering.
<b>Acceptance Criteria</b>	SRS-026 shall be tested with a Butterworth filtering method.
<b>Rationale</b>	Butterworth algorithm is known and tested and accepted as state of the art.
<b>Reference (SD,SRS,RSK)</b>	SD-04, SRS-026, SD-27
<b>Priority</b>	-
<b>Criticality</b>	-
<b>Non-functional RQ</b>	See SRS-026

<b>Tag # / CC</b>	SRS-028
<b>Requirement</b>	The system must provide functionalities to spatially filter the signal. The filtered signal is composed of channels being a linear combination of input channels.
<b>Acceptance Criteria</b>	Known signal with constant values is sent to the spatial filter with several different settings. The output values are thus known and must be equal to the expected results.
<b>Rationale</b>	Several signal processing methods rely on spatial filtering. Referencing, for example is a particular case of spatial filtering.
<b>Reference (SD,SRS,RSK)</b>	SD-04, SD-37
<b>Priority</b>	-
<b>Criticality</b>	-
<b>Non-functional RQ</b>	<ol style="list-style-type: none"> <li>1. Configurability: Spatial filter coefficients.</li> <li>2. Performance: The complexity of the algorithm must be linear in regards to the number of channels and sampling rate.</li> </ol>

<b>Tag # / CC</b>	SRS-029
<b>Requirement</b>	The system shall be able to generate xDAWN spatial filter coefficients from an appropriately marked signal. The algorithm is described in the scientific paper: <a href="http://www.ncbi.nlm.nih.gov/pubmed/19174332">http://www.ncbi.nlm.nih.gov/pubmed/19174332</a> .
<b>Acceptance Criteria</b>	A signal with labeled P300 brain responses is sent to the filter trainer. An LDA classifier is trained on spatially filtered signal and on non-spatially filtered signal. Afterwards, a classification challenge with both of the classifiers must yield better results with the spatially filtered signal.
<b>Rationale</b>	xDAWN spatial filter is state of the art in pattern-recognition based BCIs such as P300.
<b>Reference (SD,SRS,RSK)</b>	SD-04, SD-29
<b>Priority</b>	-
<b>Criticality</b>	-
<b>Non-functional RQ</b>	See SRS-028

<b>Tag # / CC</b>	SRS-030
<b>Requirement</b>	The system shall be able to generate CSP (Common Spatial Pattern) spatial filter coefficients from an appropriately marked signal. The algorithm is described in the scientific paper: <a href="http://www.ncbi.nlm.nih.gov/pubmed/10400191">http://www.ncbi.nlm.nih.gov/pubmed/10400191</a> <a href="http://www.ncbi.nlm.nih.gov/pubmed/20889426">http://www.ncbi.nlm.nih.gov/pubmed/20889426</a> .
<b>Acceptance Criteria</b>	A signal with labeled Motor Imagery brain responses is sent to the filter trainer. An LDA classifier is trained on spatially filtered signal and on non-spatially filtered signal. Afterwards, a classification challenge with both of the classifiers must yield better results with the spatially filtered signal.
<b>Rationale</b>	CSP is an established technique for EEG feature extraction used as parts of nonlinear classification pipelines. It may help in situations where the class membership of the signal is expected to be reflected in the signals per-class covariance structure.
<b>Reference (SD,SRS,RSK)</b>	SD-04, SD-25
<b>Priority</b>	-
<b>Criticality</b>	-
<b>Non-functional RQ</b>	See SRS-028



<b>Tag # / CC</b>	SRS-031
<b>Requirement</b>	The system must be able to divide temporal series into blocks and then apply the various signal processing algorithms on the blocks. The blocks can be continuous, discontinuous or overlapping.
<b>Acceptance Criteria</b>	SRS-032, SRS-033
<b>Rationale</b>	Signal slicing provides many critical functionalities, e.g.: it can be used to select periods of time during which the subject was in a particular state.
<b>Reference (SD,SRS,RSK)</b>	SD-04
<b>Priority</b>	-
<b>Criticality</b>	-
<b>Non-functional RQ</b>	-

<b>Tag # / CC</b>	SRS-032
<b>Requirement</b>	The system must provide functionalities to select a block of signal based on a trigger.
<b>Acceptance Criteria</b>	A known artificial signal with known triggers (of different names) is sent to the algorithm. Selection is performed on one of the trigger names. The resulting chunks will correspond to the correct periods of the signal.
<b>Rationale</b>	Selecting the relevant signal block corresponding to a given stimulation.
<b>Reference (SD,SRS,RSK)</b>	SD-04, SD-35
<b>Priority</b>	-
<b>Criticality</b>	-
<b>Non-functional RQ</b>	<ol style="list-style-type: none"> <li>1. Configurability: Block duration, block start time offset from the trigger occurrence time and trigger type.</li> <li>2. Performance: This procedure must have no impact on the signal samples and no significant processing impact.</li> </ol>

<b>Tag # / CC</b>	SRS-033
<b>Requirement</b>	The system must provide functionalities to select a block of signal on a regular time basis.
<b>Acceptance Criteria</b>	A known, artificially constructed signal is sent to the algorithm and sliced in different manners. The output slices should correspond to the correct spots in the original signal.
<b>Rationale</b>	Selecting the relevant signal block corresponding to a time point.
<b>Reference (SD,SRS,RSK)</b>	SD-04, SD-24
<b>Priority</b>	-
<b>Criticality</b>	-
<b>Non-functional RQ</b>	<ol style="list-style-type: none"> <li>1. Configurability: Block duration, block start time offset from the trigger occurrence time and trigger type.</li> <li>2. Performance: This procedure must have no impact on the signal samples and no significant processing impact.</li> </ol>

<b>Tag # / CC</b>	SRS-034
<b>Requirement</b>	The system must provide functionalities to apply windowing methods on a block of signal.
<b>Acceptance Criteria</b>	Known artificial signal is first temporally epoched and then windowed. The resulting signal must be equal to signal which was processed in the same manner using different, tested and accepted tools.
<b>Rationale</b>	Preprocessing before spectrum analysis in order to reduce spectral leakage.
<b>Reference (SD,SRS,RSK)</b>	SD-04, SD-28
<b>Priority</b>	-
<b>Criticality</b>	-
<b>Non-functional RQ</b>	<ol style="list-style-type: none"> <li>1. Configurability: Windowing method.</li> </ol>

<b>Tag # / CC</b>	SRS-035
<b>Requirement</b>	The system must provide functionalities to compute spectral analysis on the signal using Fast Fourier Transform.
<b>Acceptance Criteria</b>	Artificial signal with known frequency components is created. This signal is sent to the FFT algorithm. The resulting spectrum must present the original components.
<b>Rationale</b>	Spectral analysis is key to many different signal processing methods. It is also a very useful monitoring tool.
<b>Reference (SD,SRS,RSK)</b>	SD-04, SD-26
<b>Priority</b>	-
<b>Criticality</b>	-
<b>Non-functional RQ</b>	1. Performance: The algorithm must have performance on par with the state of the art.

<b>Tag # / CC</b>	SRS-036
<b>Requirement</b>	The system must provide functionalities to compute the average of all the frequency band on a spectrum. The average is calculated with the following rule: chunks over time.  <u>Assumption / Prerequisites :</u> SRS-035 is fulfilled.
<b>Acceptance Criteria</b>	A known artificial spectrum is averaged. The resulting value should be equal to previously calculated and validated average.
<b>Rationale</b>	Averaging increases robustness when working with signal containing noise.
<b>Reference (SD,SRS,RSK)</b>	SD-04, SRS-035, SD-35
<b>Priority</b>	-
<b>Criticality</b>	-
<b>Non-functional RQ</b>	1. Configurability: Frequency band.

<b>Tag # / CC</b>	SRS-037
<b>Requirement</b>	The system must provide functionalities to vote the most frequent stimulus in a given time window and send the selected stimulus as output.
<b>Acceptance Criteria</b>	Simulated stimulations are sent to the voter. After the configured number of stimulations, the voter should emit the correct stimulation.
<b>Rationale</b>	Classification according to the frequency of an event.
<b>Reference (SD,SRS,RSK)</b>	SD-04, SD-43
<b>Priority</b>	-
<b>Criticality</b>	-
<b>Non-functional RQ</b>	<ol style="list-style-type: none"> <li>1. Configurability: Minimum number of stimuli to perform a vote, width of the time window, possibility to discard a stimuli after its participation to a vote, possibility to tag the output stimulus with time of vote, last occurrence of voted stimulus or time of the last participant to the vote, possibility to define a class of rejected stimulation types that can or cannot win the vote.</li> </ol>

<b>Tag # / CC</b>	SRS-039
<b>Requirement</b>	The system should provide a way to select a subset of a spectrum matrix, turning all unselected frequency band to 0.
<b>Acceptance Criteria</b>	A full band spectrum is sent to the frequency band selector. After the configured frequency selected, all other bands should be set to 0.
<b>Rationale</b>	Preserves some spectrum coefficients and puts the others to zero depending on a list of frequencies / frequency bands to select
<b>Reference (SD,SRS,RSK)</b>	SD-38
<b>Priority</b>	-
<b>Criticality</b>	-
<b>Non-functional RQ</b>	-

## 9. Non-Functional Requirements

### 9.1 Usability/Maintainability

<b>Tag # / CC</b>	SRS-500
<b>Requirement</b>	<p>The system must provide documentation for QA team members. Documentation must comply with class B software as defined in IEC 63604. This documentation must include:</p> <ul style="list-style-type: none"><li>• Software Development Plan</li><li>• Software Architecture</li><li>• Software Detailed Specifications</li><li>• User Guide</li><li>• Test Plan</li><li>• Tests Plan Reports</li><li>• Requirements</li><li>• Software Definition</li><li>• Risk Management</li><li>• Release Note</li><li>• Boxes Documentation</li><li>• Doxygen Documentation</li><li>• Automated Unitary Tests</li><li>• Quick User Guide</li><li>• Test Program</li><li>• Test Program User Guide</li></ul>
<b>Acceptance Criteria</b>	Check that all required documents as defined in the requirement are available in the delivery package.
<b>Rationale</b>	The software must be delivered with enough documentation to be used in a certified medical device without additional justification.
<b>Reference (SD,SRS,RSK)</b>	SD-01, SD-02
<b>Priority</b>	-
<b>Criticality</b>	-

<b>Tag # / CC</b>	SRS-501
<b>Requirement</b>	<p>The system must provide documentation for scientists. This documentation must include:</p> <ul style="list-style-type: none"> <li>• Install procedure</li> <li>• Quick start guide with basic commands to build and run a pipeline</li> <li>• Public API reference guide</li> <li>• Detailed documentation of all scientific features</li> <li>• Release Note</li> <li>• Boxes Documentation</li> <li>• Doxygen Documentation</li> <li>• Automated Unitary Tests</li> <li>• Quick User Guide</li> <li>• Test Program</li> <li>• Test Program User Guide</li> </ul> <p>This package of documentation should be stored and available in a dedicated space define by the project team during the delivery step.</p>
<b>Acceptance Criteria</b>	Check that all required documents as defined in the requirement are available in the delivery package. The delivery package will be available in a storage space define by the project (different between SRS-501 and 502).
<b>Rationale</b>	Usability from a scientist perspective. He should have enough documentation to get started with the system.
<b>Reference (SD,SRS,RSK)</b>	SD-07
<b>Priority</b>	-
<b>Criticality</b>	-



<b>Tag # / CC</b>	SRS-502
<b>Requirement</b>	<p>The system must provide documentation for developers. This documentation must include:</p> <ul style="list-style-type: none"> <li>• Install procedure</li> <li>• Architecture and design documentation</li> <li>• Full API reference guide</li> <li>• Release Note</li> <li>• Boxes Documentation</li> <li>• Doxygen Documentation</li> <li>• Automated Unitary Tests</li> <li>• Quick User Guide</li> <li>• Test Program</li> <li>• Test Program User Guide</li> </ul> <p>This package of documentation should be stored and available in a dedicated space define by the project team during the delivery step.</p>
<b>Acceptance Criteria</b>	Check that all required documents as defined in the requirement are available in the delivery package. The delivery package will be available in a storage space define by the project (different between SRS-501 and 502).
<b>Rationale</b>	Understandability. It should be easy for future developers to understand how the system works.
<b>Reference (SD,SRS,RSK)</b>	SD-07
<b>Priority</b>	-
<b>Criticality</b>	-

<b>Tag # / CC</b>	SRS-503
<b>Requirement</b>	The system must provide all services and functionalities through a set of C++ APIs.
<b>Acceptance Criteria</b>	Check that APIs are developed in C++ and distributed as a set of APIs.
<b>Rationale</b>	C++ is the base high-performance language used to develop medical device.
<b>Reference (SD,SRS,RSK)</b>	SD-03
<b>Priority</b>	-
<b>Criticality</b>	-

<b>Tag # / CC</b>	SRS-527
<b>Requirement</b>	The system should provide a way to help on debugging and check the validity of a streamed or derived matrix.
<b>Acceptance Criteria</b>	Checks if a matrix contains "not a number" or "infinity" elements
<b>Rationale</b>	The developer and tester need tools to validate the conformance of stream matrix.
<b>Reference (SD,SRS,RSK)</b>	SD-49
<b>Priority</b>	-
<b>Criticality</b>	-
<b>Non-functional RQ</b>	-

<b>Tag # / CC</b>	SRS-528
<b>Requirement</b>	The system should provide a way to help on debugging and log the stimulation received. The log message will be logged according to a log level set.
<b>Acceptance Criteria</b>	Use a clock stimulator to a stimulation listener. Now chose an appropriate log level for the stimulation listener and start a processing.
<b>Rationale</b>	The developer and tester need tools to log the stimulation codes received in readable way.
<b>Reference (SD,SRS,RSK)</b>	SD-50
<b>Priority</b>	-
<b>Criticality</b>	-
<b>Non-functional RQ</b>	1. Performance: Reduce log verbosity so the application could be real timed.

<b>Tag # / CC</b>	SRS-529
<b>Requirement</b>	The system should provide a way to help on debugging, by recording the structure of an EBML stream.
<b>Acceptance Criteria</b>	Use a reference EBML stream with a known and validated structure and compare with the output generated.
<b>Rationale</b>	The developer and tester need tools to have the EBML stream tree in readable way.
<b>Reference (SD,SRS,RSK)</b>	SD-48
<b>Priority</b>	-
<b>Criticality</b>	-
<b>Non-functional RQ</b>	1. Performance: Reduce log verbosity so the application could be real timed.



## 9.2 Changeability

<b>Tag # / CC</b>	SRS-504
<b>Requirement</b>	The software must be distributed as open-source.
<b>Acceptance Criteria</b>	Check that the source code is available in the delivery package.
<b>Rationale</b>	Some benefits of open-source in this case: auditability. Customizability is not in the list because any change in the source code will make associated documentation obsolete (e.g. tests reports, risk analysis).
<b>Reference (SD,SRS,RSK)</b>	SD-10
<b>Priority</b>	-
<b>Criticality</b>	-

<b>Tag # / CC</b>	SRS-505
<b>Requirement</b>	The software must be easy to extend. Addition of new processing modules should be possible without recompilation of the whole source code.
<b>Acceptance Criteria</b>	Check it is possible to add a new filter without recompiling the source code.
<b>Rationale</b>	Extensibility. New components can be easily added later.
<b>Reference (SD,SRS,RSK)</b>	SD-06
<b>Priority</b>	-
<b>Criticality</b>	-

### 9.3 Testability

<b>Tag # / CC</b>	SRS-506
<b>Requirement</b>	The system must provide functionalities to run unit tests automatically.
<b>Acceptance Criteria</b>	Check unit tests are scriptable i.e. it can be run from command line.
<b>Rationale</b>	Automatic testing.
<b>Reference (SD,SRS,RSK)</b>	SD-07
<b>Priority</b>	-
<b>Criticality</b>	-

<b>Tag # / CC</b>	SRS-507
<b>Requirement</b>	Building and running unit test should be fast.
<b>Acceptance Criteria</b>	Building and running all unit tests at code level should be less than 5mn. Integration tests that involve multiple components are not taken into account.
<b>Rationale</b>	Continuous Integration. Fast building unit testing allows quick feedback on commit stage. Traditionally, unit tests are run on each commit while longer integration tests are run on another time scale (e.g. nightly).
<b>Reference (SD,SRS,RSK)</b>	SD-07
<b>Priority</b>	-
<b>Criticality</b>	-

<b>Tag # / CC</b>	SRS-508
<b>Requirement</b>	The system must provide functionalities to test processing pipelines automatically.
<b>Acceptance Criteria</b>	Check integration and regression tests are scriptable i.e. it can be run from command line.
<b>Rationale</b>	Automatic integration/regression testing.
<b>Reference (SD,SRS,RSK)</b>	SD-07
<b>Priority</b>	-
<b>Criticality</b>	-

<b>Tag # / CC</b>	SRS-509
<b>Requirement</b>	Running tests must not require extra-tools.
<b>Acceptance Criteria</b>	Install software system and test project on a clean platform. Check launching tests requires nothing else than the software system and corresponding dependencies.
<b>Rationale</b>	Portability of tests. There should be no need for platform-specific tool (other than tools shipped with the system) to run the tests.
<b>Reference (SD,SRS,RSK)</b>	SD-07
<b>Priority</b>	-
<b>Criticality</b>	-

## 9.4 Reliability

<b>Tag # / CC</b>	SRS-510
<b>Requirement</b>	<p>The system must not find itself in erroneous state that is not detectable. If a component of the system detects an error, the cause of the error must be communicated to the caller component. Any error should be log for inspection.</p> <p>Error management must provide a comprehensive way to identify the error type and the criticality of the error.</p> <p>The way of handling error must be configurable according to the criticality and can be changed dynamically.</p>
<b>Acceptance Criteria</b>	<p>A scenario corrupted in different manners will be loaded, this error must be detected.</p> <p>Acceptance of error checks of all signal processing plugins.</p>
<b>Rationale</b>	<p>Error management is required to comply with medical device standards. Moreover, it is a useful tool for debugging (maintainability), testing (testability). User of the software system can rely on the system error management to build a user-friendly comprehensive error system for the end-user (usability).</p>
<b>Reference (SD,SRS,RSK)</b>	SD-01, SD-02
<b>Priority</b>	-
<b>Criticality</b>	-



<b>Tag # / CC</b>	SRS-511
<b>Requirement</b>	If a running signal processing pipeline detects an error, it must stop and communicate the reason for doing so. It must not continue processing in erroneous state.
<b>Acceptance Criteria</b>	Open and run a scenario on corrupted data. Check running is stopped because an error occurred during processing.
<b>Rationale</b>	Keeping on processing in an erroneous state is not acceptable from a medical device standards perspective.
<b>Reference (SD,SRS,RSK)</b>	SD-01 SD-02
<b>Priority</b>	-
<b>Criticality</b>	-

<b>Tag # / CC</b>	SRS-512
<b>Requirement</b>	If a running signal processing pipeline fails to process data in real-time, it must report the problem to the user.  The maximum latency of the pipeline is configurable.
<b>Acceptance Criteria</b>	Run a heavy-duty processing pipeline and check the system reports an error to comply with real-time processing expectations.
<b>Rationale</b>	The system user must be aware the processing pipeline load is too heavy. The system user will be responsible to take the right decision according to its use case.
<b>Reference (SD,SRS,RSK)</b>	SD-01, SD-02
<b>Priority</b>	-
<b>Criticality</b>	-

<b>Tag # / CC</b>	SRS-513
<b>Requirement</b>	<p>The software system must stay available as long as needed by the user.</p> <p>This requirement must be true only if:</p> <ul style="list-style-type: none"> <li>• Enough platform resources (e.g. OS memory) are available when processing pipelines are all up and running (Minimum platform resources requirements MPRR),</li> <li>• MPRR do not decrease during time,</li> <li>• The processing executed is mathematically relevant.</li> </ul> <p>The minimum time of availability is: 12 Hours.</p>
<b>Acceptance Criteria</b>	On a clean platform, run a set processing pipelines in parallel and run a greedy background process that let just enough resources for MPRR to be met. Check keeps on processing data for the minimum availability time.
<b>Rationale</b>	Longevity. It should not gradually use up a limited resource. Example longevity defects include memory leaks or filling the disk with log files.
<b>Reference (SD,SRS,RSK)</b>	SD-01, SD-02
<b>Priority</b>	-
<b>Criticality</b>	-

<b>Tag # / CC</b>	SRS-514
<b>Requirement</b>	If the processing load rises, the software must not lose or corrupt data chunk in the limits of physical hardware.
<b>Acceptance Criteria</b>	Run a heavy-duty processing pipeline that does not meet real-time requirement (see SRS-512) and check no data is lost or corrupted.
<b>Rationale</b>	Consistency under load.
<b>Reference (SD,SRS,RSK)</b>	SD-01, SD-02
<b>Priority</b>	-
<b>Criticality</b>	-

<b>Tag # / CC</b>	SRS-515
<b>Requirement</b>	Install and deployment errors must be reported to the user so that he is able to handle the problem.
<b>Acceptance Criteria</b>	Set an environment that makes the install procedure fails and check error is reported with detailed explanations.
<b>Rationale</b>	Install can fail but the user must be aware of it as he should not use the system afterwards.
<b>Reference (SD,SRS,RSK)</b>	SD-01, SD-02
<b>Priority</b>	-
<b>Criticality</b>	-

## 9.5 Performance

<b>Tag # / CC</b>	SRS-517
<b>Requirement</b>	<p>The processing pipeline must provide low latency out of processing steps (only data streaming is taken into account). This low latency must not decrease with time.</p> <p>For a typical neurofeedback pipeline, end-to-end per data chunk processing must be kept constant from start to finish with no increase of memory resources except for events such as triggering of classifier training.</p>
<b>Acceptance Criteria</b>	Run minimal processing pipeline during the minimum limit of time. Check benchmark results are compatible with requirements.
<b>Rationale</b>	Low latency and low jitter out of processing steps.
<b>Reference (SD,SRS,RSK)</b>	SD-04
<b>Priority</b>	-
<b>Criticality</b>	-

<b>Tag # / CC</b>	SRS-519
<b>Requirement</b>	<p>Cold system start time must be kept under 10000ms. Starting procedure includes:</p> <ul style="list-style-type: none"> <li>• Loading the kernel</li> <li>• Initializing the kernel</li> </ul> <p>Target Environment: Surface pro 3 with intel i5 processor (no VM)</p>
<b>Acceptance Criteria</b>	Execute a process that loads and initialize the kernel. Check benchmark results are compatible with requirements.
<b>Rationale</b>	Low latency at system start-up.
<b>Reference (SD,SRS,RSK)</b>	SD-08
<b>Priority</b>	-
<b>Criticality</b>	-

## 9.6 Interoperability

<b>Tag # / CC</b>	SRS-520
<b>Requirement</b>	Processing pipelines are compatible with OpenViBE software (version 1.1.0)
<b>Acceptance Criteria</b>	<p>Open an OpenViBE scenario with the system.</p> <p>Open an OpenViBE scenario with the specific Identity box inside.</p> <p>Open a CertiViBE scenario with OpenViBE must work as long as it does not use features only available in CertiViBE.</p>
<b>Rationale</b>	See SDD-11 Rationale.
<b>Reference (SD,SRS,RSK)</b>	SD-11, SRS-009, SD-20
<b>Priority</b>	-
<b>Criticality</b>	-

<b>Tag # / CC</b>	SRS-521
<b>Requirement</b>	Signal data recorded with the system must be compatible with OpenViBE software (version 1.1.0)
<b>Acceptance Criteria</b>	Open signal data recorded with OpenViBE with the system. Open signal data recorded with CertiViBE with OpenViBE.
<b>Rationale</b>	See SDD-12 Rationale.
<b>Reference (SD,SRS,RSK)</b>	SD-12, SRS-008
<b>Priority</b>	-
<b>Criticality</b>	-

<b>Tag # / CC</b>	SRS-522
<b>Requirement</b>	Processing pipelines are compatible with NeuroRT software (version 2.4.0)
<b>Acceptance Criteria</b>	Open a NeuroRT scenario with the system. Open a NeuroRT scenario with the specific Identity box inside. Open a CertiViBE scenario with NeuroRT must work as long as it does not use features only available in CertiViBE.
<b>Rationale</b>	See SDD-08 Rationale.
<b>Reference (SD,SRS,RSK)</b>	SD-08, SRS-009, SD-20
<b>Priority</b>	-
<b>Criticality</b>	-

<b>Tag # / CC</b>	SRS-523
<b>Requirement</b>	Signal data recorded with CertiVibe must be compatible with NeuroRT software (version 2.4.0)
<b>Acceptance Criteria</b>	Open signal data recorded with NeuroRT with the system. Open signal data recorded with CertiViBE with NeuroRT.
<b>Rationale</b>	See SDD-08 Rationale.
<b>Reference (SD,SRS,RSK)</b>	SD-08, SRS-008
<b>Priority</b>	-
<b>Criticality</b>	-

<b>Tag # / CC</b>	SRS-524
<b>Requirement</b>	Processing pipeline generated by another application can be imported in the system. The software system must only provide an extension mechanism that allows the user to define and use new software components developed for a specific processing pipeline format.
<b>Acceptance Criteria</b>	Add to the system component an importer for a new file format. Check it is possible to open a scenario that follows this format.
<b>Rationale</b>	Interoperability.
<b>Reference (SD,SRS,RSK)</b>	SD-06
<b>Priority</b>	-
<b>Criticality</b>	-

<b>Tag # / CC</b>	SRS-525
<b>Requirement</b>	The system must be able to work on systems that are not in Latin based languages, such as Japanese, Chinese and Korean. This includes loading files from locations that contain non-ASCII characters, loading files whose names contain non-ASCII characters and so on.
<b>Acceptance Criteria</b>	Open scenario files whose path, name and content contain non-ASCII characters.
<b>Rationale</b>	Internationalization. The software should not restrict client applications to specific languages.
<b>Reference (SD,SRS,RSK)</b>	SD-03, SD-04
<b>Priority</b>	-
<b>Criticality</b>	-



<b>Tag # / CC</b>	SRS-530
<b>Requirement</b>	Plugin's identifier is unique and there is no conflict with an existing identifier in NeuroRT software. Define a CertiViBE range of identifier without OpenViBE identifier.
<b>Acceptance Criteria</b>	Extract all plugin identifier form OpenViBE and CertiViBE, and check all are unique.
<b>Rationale</b>	Importing a plugin from OpenViBE would be seamless as possible.
<b>Reference (SD,SRS,RSK)</b>	SD-09
<b>Priority</b>	-
<b>Criticality</b>	-

<b>Tag # / CC</b>	SRS-531
<b>Requirement</b>	Plugin's identifier is unique and there is no conflict with an existing identifier in NeuroRT software. Define a CertiViBE range of identifier without NeuroRT identifier.
<b>Acceptance Criteria</b>	Extract all plugin identifier form NeuroRT and CertiViBE, and check all are unique.
<b>Rationale</b>	Importing a plugin from NeuroRT would be seamless as possible.
<b>Reference (SD,SRS,RSK)</b>	SD-09
<b>Priority</b>	-
<b>Criticality</b>	-

<b>Tag # / CC</b>	SRS-532
<b>Requirement</b>	The integration of a plugin (or box) from OpenViBE should be done without (or at least as possible) changing the source code of the plugin.
<b>Acceptance Criteria</b>	Successful build of CertiViBE software with an OpenViBE plugin out of the project perimeter.
<b>Rationale</b>	Importing a plugin from OpenViBE would be seamless as possible. A software could be built with CertiViBE and OpenVibe's plugin.
<b>Reference (SD,SRS,RSK)</b>	SD-09
<b>Priority</b>	-
<b>Criticality</b>	-

<b>Tag # / CC</b>	SRS-533
<b>Requirement</b>	The integration of a plugin (or box) from NeuroRT should be done without (or at least as possible) changing the source code of the plugin.
<b>Acceptance Criteria</b>	Successful build of CertiViBE software with a NeuroRT plugin out of the project perimeter.
<b>Rationale</b>	Importing a plugin from NeuroRT would be seamless as possible. A software could be built with CertiViBE and NeuroRT's plugin.
<b>Reference (SD,SRS,RSK)</b>	SD-09
<b>Priority</b>	-
<b>Criticality</b>	-

## 9.7 Portability

<b>Tag # / CC</b>	SRS-526
<b>Requirement</b>	The system is available under Windows and Linux platforms. Supported OS versions must be: <ul style="list-style-type: none"><li>• Windows 7, Windows 8, Windows 10</li><li>• Linux Ubuntu 14.04, Linux Ubuntu 16.04</li></ul>
<b>Acceptance Criteria</b>	Install the software and run integration tests on all supported platform.
<b>Rationale</b>	The software system should not dictate the platform the client application should be developed on. Windows portability is required because it covers the largest set of potential users while Linux is a popular platform for scientific development. Mac OS is not supported but portability should be easy as it is a Unix system.
<b>Reference (SD,SRS,RSK)</b>	SD-05
<b>Priority</b>	-
<b>Criticality</b>	-

## 10. Requirement Traceability

### 10.1 Traceability between Software Definition and Software Requirement Specification

SD	SRS
SD-01	SRS-500 SRS-510 SRS-511 SRS-512 SRS-513 SRS-514 SRS-515 SRS-516
SD-02	SRS-500 SRS-510 SRS-511 SRS-512 SRS-513 SRS-514 SRS-515 SRS-516
SD-03	SRS-525
SD-04	SRS-001 SRS-002 SRS-003 SRS-009 SRS-010 SRS-011 SRS-012 SRS-013 SRS-014 SRS-015 SRS-016 SRS-017 SRS-018 SRS-019 SRS-020 SRS-021 SRS-022 SRS-023 SRS-024 SRS-025 SRS-026 SRS-027 SRS-028 SRS-029 SRS-030 SRS-031 SRS-032 SRS-033 SRS-034 SRS-035 SRS-036 SRS-037 SRS-517 SRS-518 SRS-525
SD-05	SRS-526
SD-06	SRS-524
SD-07	SRS-004 SRS-005 SRS-501 SRS-502 SRS-503 SRS-504 SRS-506 SRS-507 SRS-508 SRS-509
SD-08	SRS-519 SRS-522
SD-09	SRS-007 SRS-523 SRS-530 SRS-531 SRS-532 SRS-533
SD-10	SRS-505
SD-11	SRS-520
SD-12	SRS-521
SD-13	SRS-017
SD-14	SRS-017
SD-15	SRS-017
SD-16	SRS-008
SD-17	SRS-008
SD-18	SRS-012
SD-19	SRS-011
SD-20	SRS-520 SRS-522
SD-21	SRS-025
SD-22	SRS-019
SD-23	SRS-024
SD-24	SRS-033
SD-25	SRS-030
SD-26	SRS-035
SD-27	SRS-027
SD-28	SRS-034
SD-29	SRS-029
SD-30	SRS-023
SD-31	SRS-019

SD	SRS
SD-32	SRS-022
SD-33	SRS-021
SD-34	SRS-020
SD-35	SRS-032 SRS-036
SD-36	SRS-020
SD-37	SRS-028
SD-38	SRS-039
SD-39	SRS-019
SD-40	SRS-025
SD-41	SRS-003
SD-42	SRS-016
SD-43	SRS-037
SD-44	SRS-013
SD-45	SRS-014
SD-46	SRS-015
SD-47	SRS-015
SD-48	SRS-529
SD-49	SRS-527
SD-50	SRS-528
SD-51	SRS-017
SD-52	SRS-006 SRS-007
SD-53	SRS-038
SD-54	SRS-038

## 10.2 Traceability between Software Requirement Specification and Software Definition

SRS	SD
SRS-001	SD-04
SRS-002	SD-04
SRS-003	SD-04 SD-41
SRS-004	SD-07
SRS-005	SD-07
SRS-006	SD-52
SRS-007	SD-09 SD-52
SRS-008	SD-16 SD-17
SRS-009	SD-04
SRS-010	SD-04
SRS-011	SD-04 SD-19
SRS-012	SD-04 SD-18
SRS-013	SD-04 SD-44
SRS-014	SD-04 SD-45
SRS-015	SD-04 SD-46 SD-47
SRS-016	SD-04 SD-42
SRS-017	SD-04 SD-13 SD-14 SD-15 SD-51
SRS-018	SD-04
SRS-019	SD-04 SD-31 SD-22 SD-39
SRS-020	SD-04 SD-34 SD-36
SRS-021	SD-04 SD-33
SRS-022	SD-04 SD-32
SRS-023	SD-04 SD-30
SRS-024	SD-04 SD-23
SRS-025	SD-04 SD-40 SD-21
SRS-026	SD-04
SRS-027	SD-04 SD-27
SRS-028	SD-04 SD-37
SRS-029	SD-04 SD-29
SRS-030	SD-04 SD-25
SRS-031	SD-04
SRS-032	SD-04 SD-35
SRS-033	SD-04 SD-24
SRS-034	SD-04 SD-28
SRS-035	SD-04 SD-26
SRS-036	SD-04 SD-35

SRS	SD
SRS-037	SD-04 SD-43
SRS-038	SD-53 SD-54
SRS-039	SD-38
SRS-500	SD-01 SD-02
SRS-501	SD-07
SRS-502	SD-07
SRS-503	SD-07
SRS-504	SD-07
SRS-505	SD-10
SRS-506	SD-07
SRS-507	SD-07
SRS-508	SD-07
SRS-509	SD-07
SRS-510	SD-01 SD-02
SRS-511	SD-01 SD-02
SRS-512	SD-01 SD-02
SRS-513	SD-01 SD-02
SRS-514	SD-01 SD-02
SRS-515	SD-01 SD-02
SRS-516	SD-01 SD-02
SRS-517	SD-04
SRS-518	SD-04
SRS-519	SD-08
SRS-520	SD-11 SD-20
SRS-521	SD-12
SRS-522	SD-08 SD-20
SRS-523	SD-09 SD-06 SD-03 SD-04
SRS-524	SD-06 SD-03 SD-04 SD-05
SRS-525	SD-03 SD-04 SD-05 SD-49
SRS-526	SD-05 SD-49 SD-50
SRS-527	SD-49 SD-50 SD-48
SRS-528	SD-50 SD-48
SRS-529	SD-48
SRS-530	SD-09
SRS-531	SD-09
SRS-532	SD-09
SRS-533	SD-09

## 11. Revision History

REVISIONS			
REVISION LEVEL	DESCRIPTION	AUTHOR	DATE
1	Creation of document	Jerome Chabrol, Charles Garraud, Jozef Legeny	13/11/2015